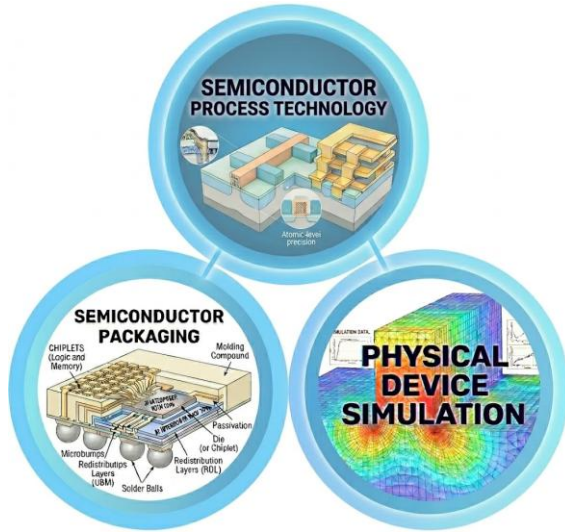




Training Program on Semiconductor Process Technology and Packaging



Organized by



Department of Electronics Engineering
 Sardar Vallabhbhai National Institute of
 Technology, Surat, Gujarat
 In association with
 Kaynes Semicon & ACDC Tech

SVNIT Surat, in association with Kaynes Semicon & ACDC Tech, is conducting a training program on **Semiconductor Process Technology & Packaging in Offline Mode**. This program aims to develop specialized manpower in Semiconductor Process Technology and Packaging in line with the **India Semiconductor Mission**. Participants will acquire deep theoretical insights combined with intensive **hands-on lab modules**.

Highlights of the course:

- Semiconductor Fab Lab Hands-on
- Expert Lecture and Packaging Demonstration
- Industrial Visit: Semicon Fab and Packaging
- TCAD Tool support under the C2S program
- Career Counseling Sessions
- Government Certificate

Eligibility:

- B.Tech: 6 semester onwards
- M.Sc: Second Year onwards
- Pursuing M.Tech./PhD in relevant areas
- Faculty members working in a relevant area

Duration: 02 Weeks

Food and accommodation may be availed at the institute's hostels on a payment basis.

Limited no of seats: 30

Registration Fee: 800/- + 18% GST (INR 944/-)

Course Coordinators:

- Dr. Deepak Joshi
- Dr. Vivek Garg
- Dr. Abhishek Acharya
- Dr. Pankaj Sharma
- Dr. Nithin Chatterji

Important Dates:

Last Date to Register: **01/06/2026**
 Date of Confirmation: **07/06/2026**
 Commencement of Course: **22/06/2026**

For Registration



For Payment



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Outline of the Program

	Topic	Duration and Timings
Semiconductor Technology and Packaging	Semiconductor Process Technology 1	2 Weeks, Monday to Saturday
	Semiconductor Process Technology 2 (Hands-On)	
	Semiconductor Packaging	
Device Simulation & Modeling	Physics of Semiconductor Devices	Industry Visit (1-2 Days)
	TCAD tool workflow	
	NMOS/PMOS/ CMOS Simulation	
	Realtime Device Analysis	

Sessions:

- Daily 6-hrs sessions:
9:30 AM – 12:30 PM
2:30 PM – 5:30 PM
- Short take-home quiz to reinforce the most important concepts taught in the day.

Additional Benefits:

- Assignments to supplement lectures with hands-on experiments on EDA tools
- Sessions by Industry Leaders from top Semiconductor Technology, Packaging, and Solar Module Manufacturers.
- Semiconductor Fab Lab Hands-on
- Summative quiz to assess learning
- Interaction with Industry leaders
- Expert Lecture and Demo of Semiconductor Packaging
- Industrial Visit: Semiconductor Fab and Packaging

For queries please contact: d.joshi@eced.svnit.ac.in, vivekg@eced.svnit.ac.in